

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jason Cheng; Cyrus Tsui; Satwant Singh; Albert Chan; Ju Shen;
Clement Lee
Assignee: Lattice Semiconductor Corporation
Title: Programmable Logic Device With Enhanced Wide Input Product
Term Cascading
Serial No.: 10/619,711 Filing Date: July 14, 2003
Examiner: James Cho Group Art Unit: 2819
Docket No.: M-9559-2P US Confirmation No.: 7093

Irvine, California
October 11, 2004

Via Facsimile to (703) 872-9306

RECEIVED
CENTRAL FAX CENTER
OCT 11 2004

Attn: Examiner James Cho

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-3514

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination of the above application, please amend the above-identified application as follows.

LAW OFFICES OF
MATTHEWSON KWOK CHEN
& HEID LLP

2402 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92612
(949) 752-7040
FAX (949) 752-7049